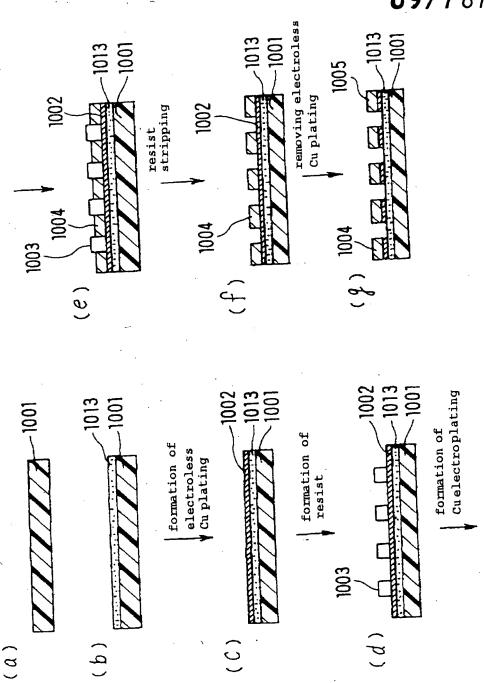
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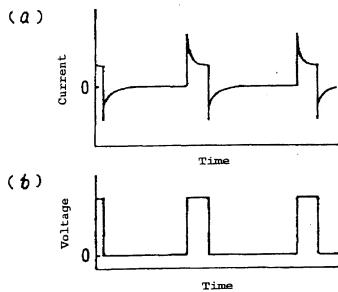


Q63452 EN, Honchin et al. Printed Wiring Board And Its Manufact g Method Date Filed: March 14, 2001 Fig. 202-293-7060 Joseph J. Ruch, Jr. 2 of 47 15 (a) 09/787139 - 1006b 1006a (b) -1011 1006b formation of openings for via holes 1007 1006a (C) 1011 1006b formation of electroless Cu plating 1008 (d) -1011 formation of Cu electroplating 1003 1003 1008 1009 (*e*) 1010 1011 1006a

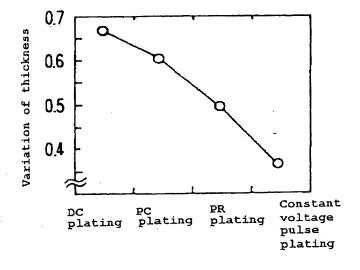
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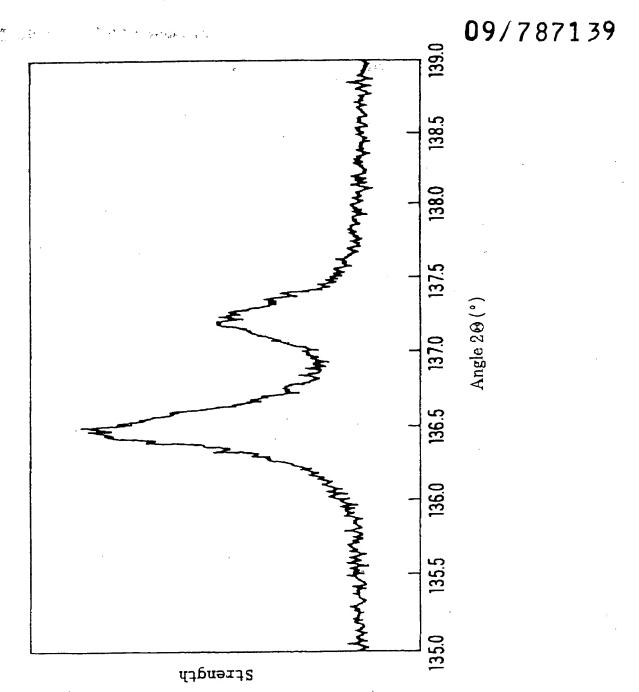


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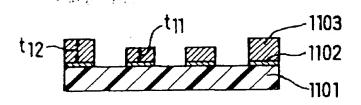
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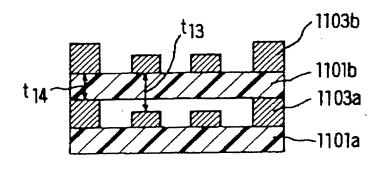
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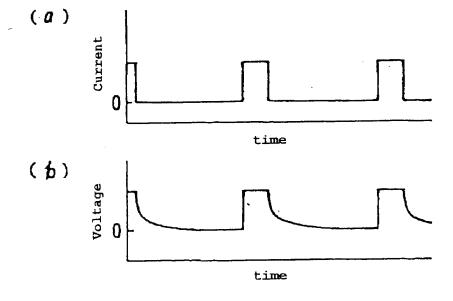
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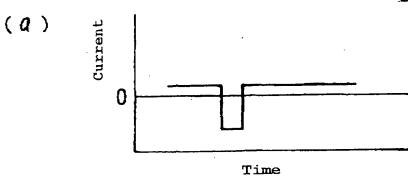


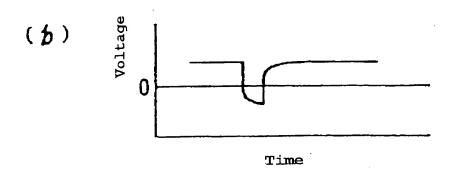
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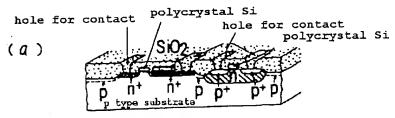
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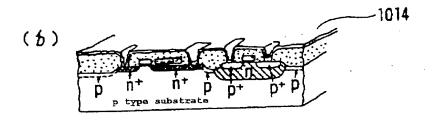
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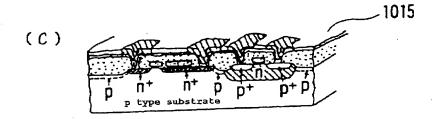
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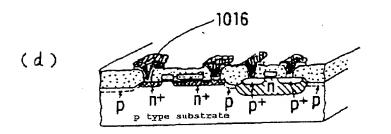
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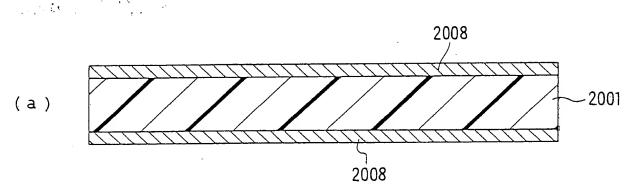
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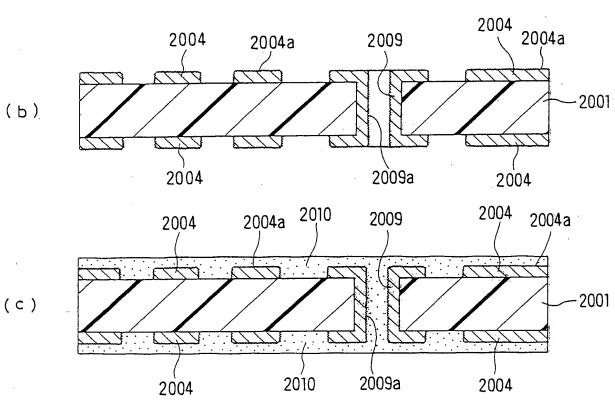
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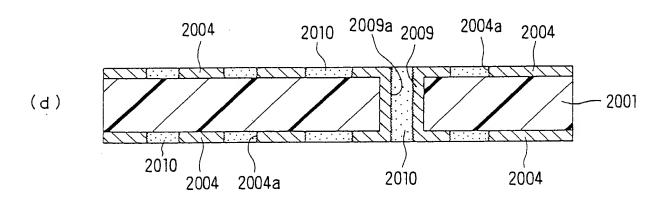
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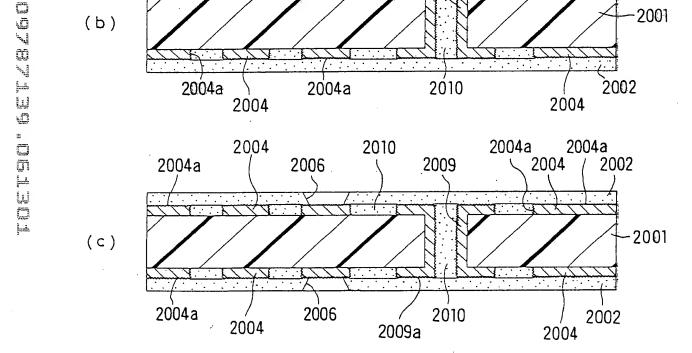
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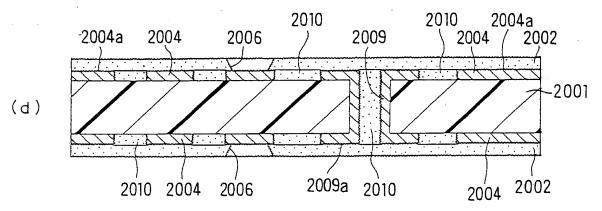
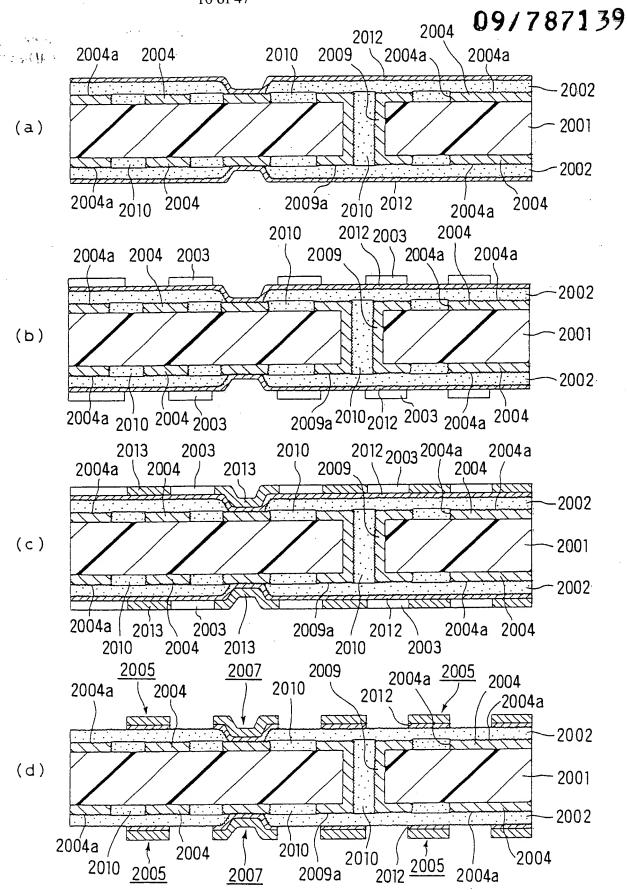


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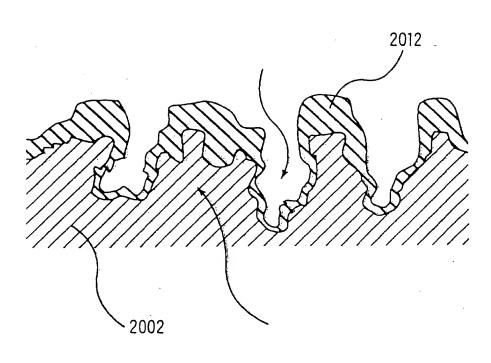
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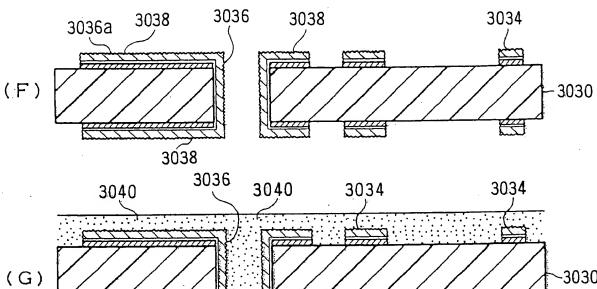
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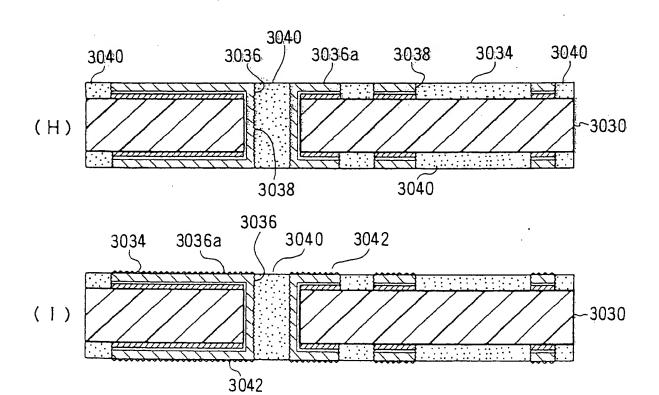
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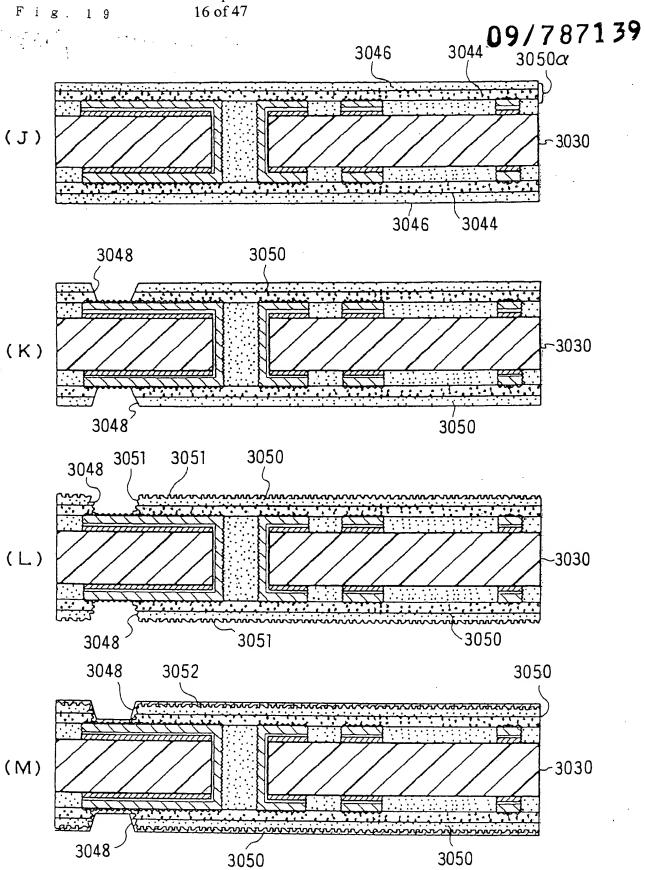
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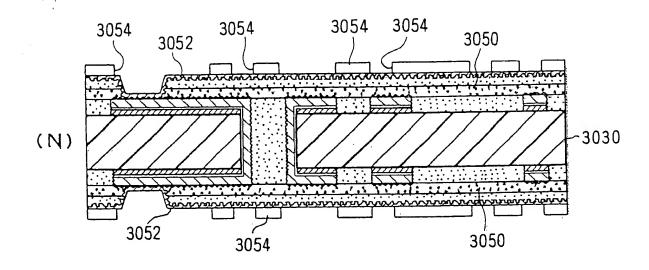
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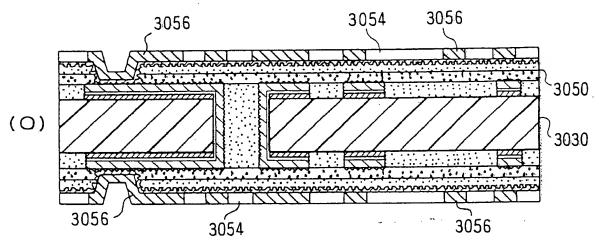
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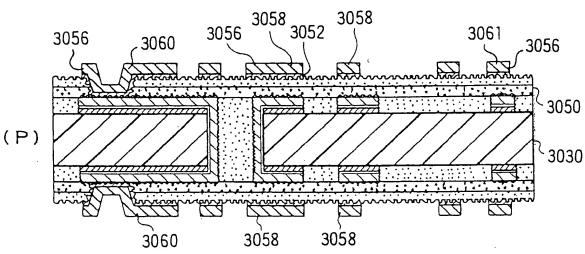
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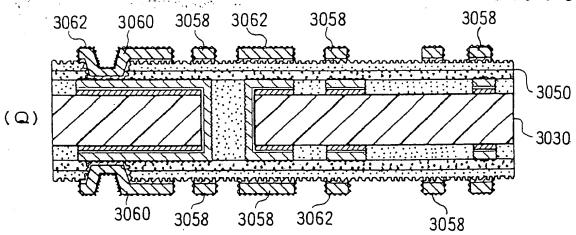


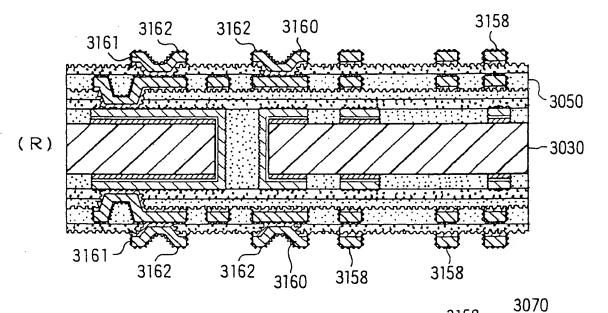
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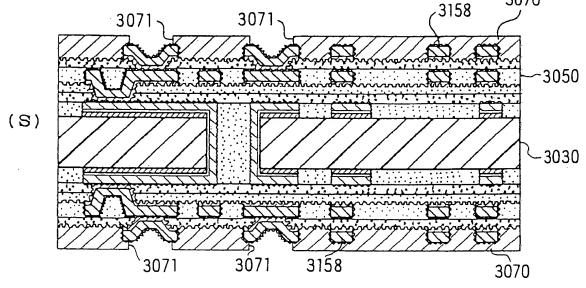
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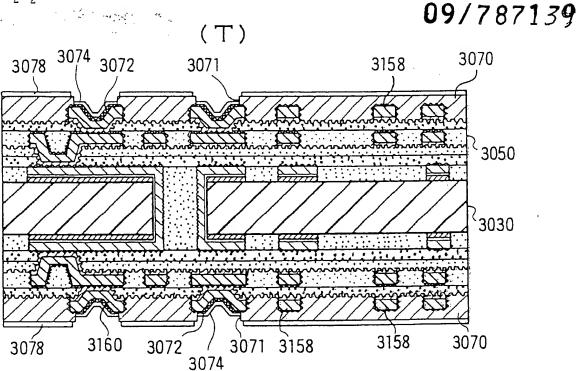


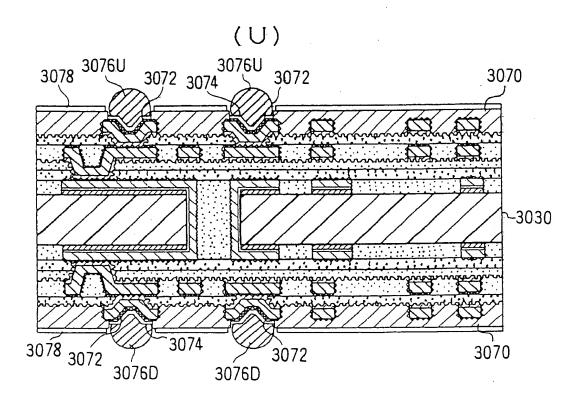
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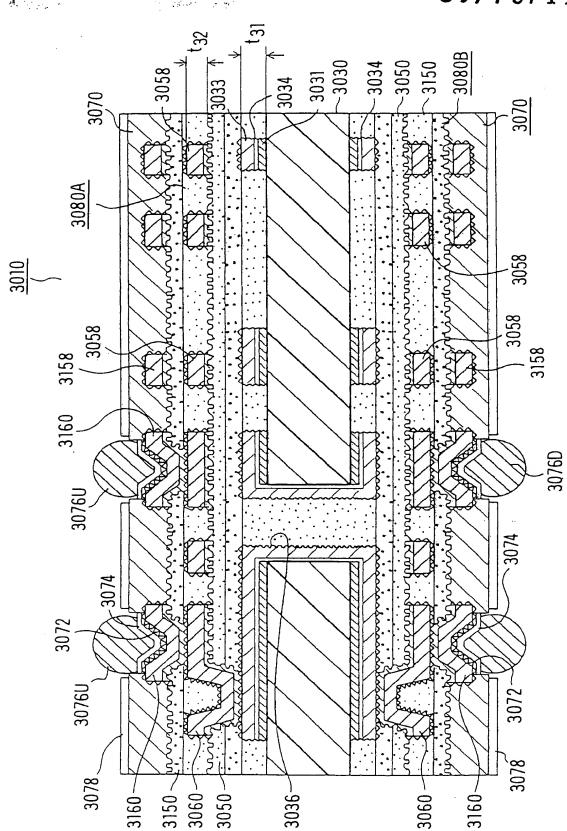
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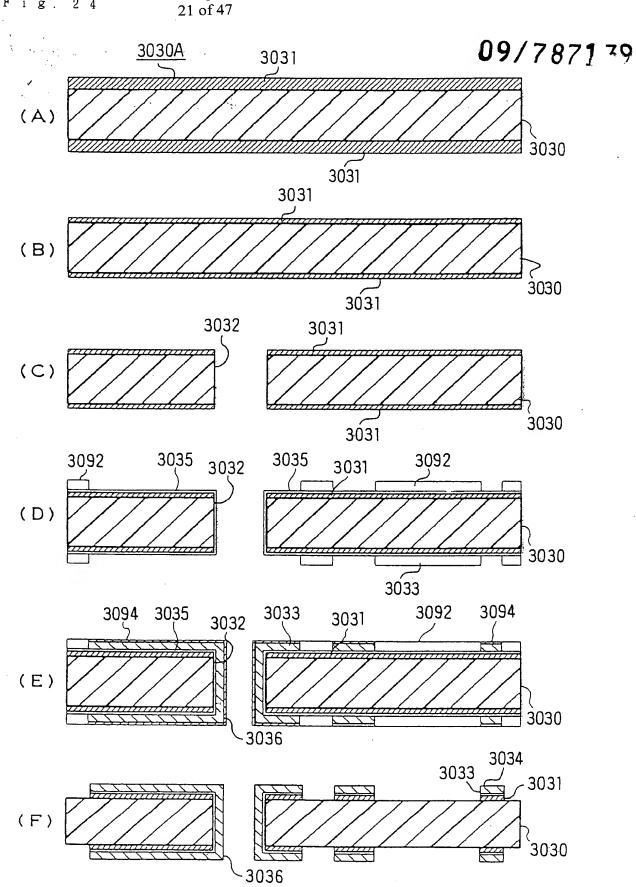
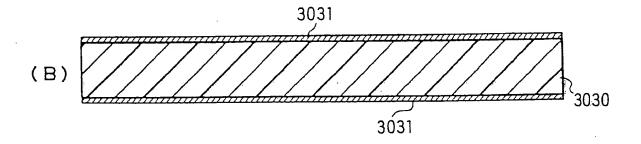


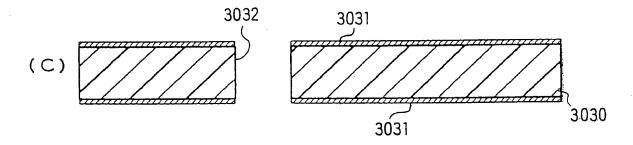
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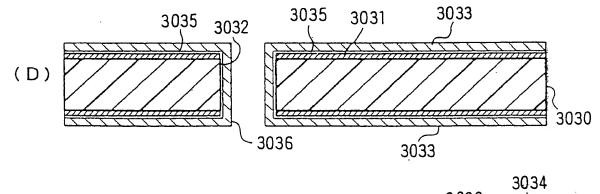
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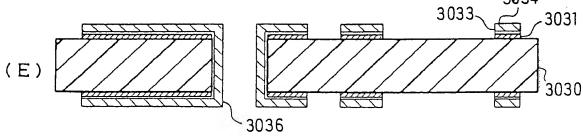


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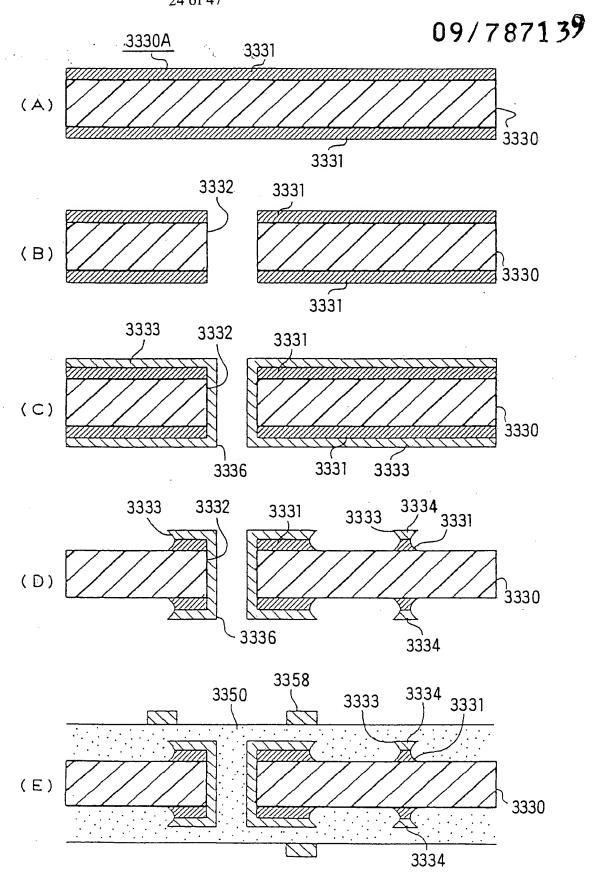
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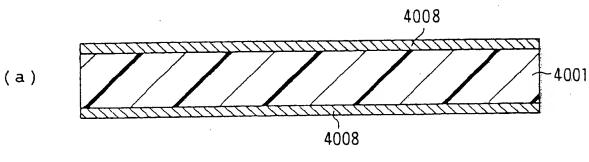
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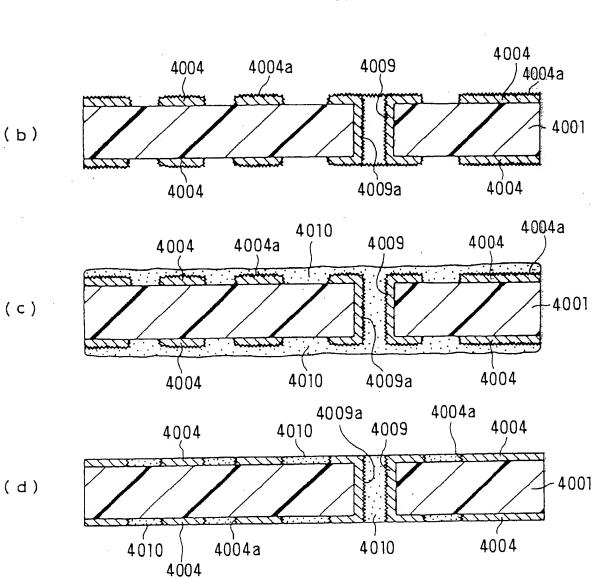
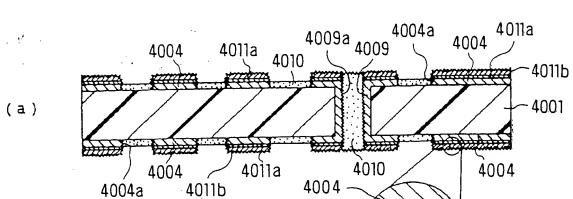


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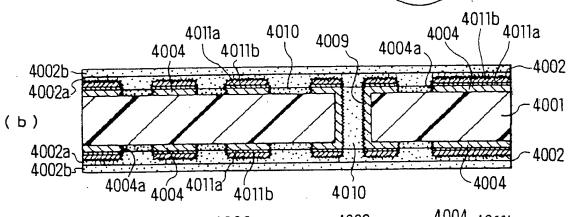
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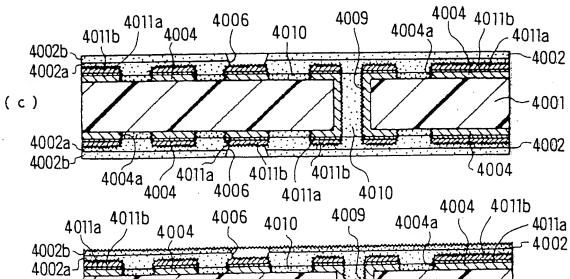
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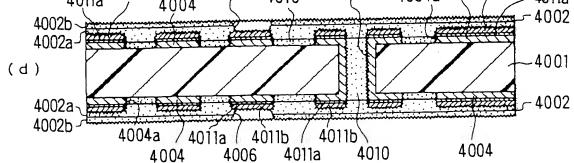


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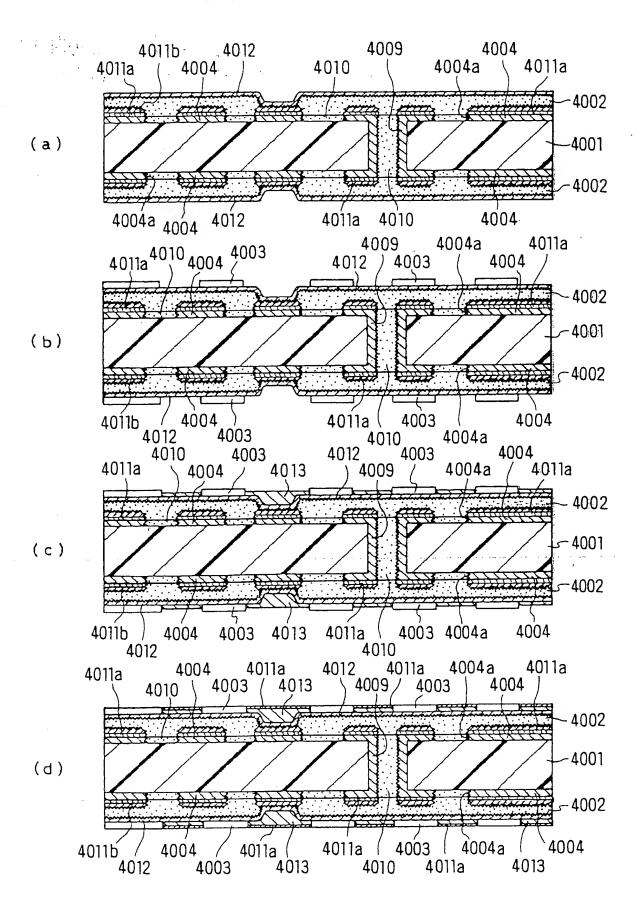




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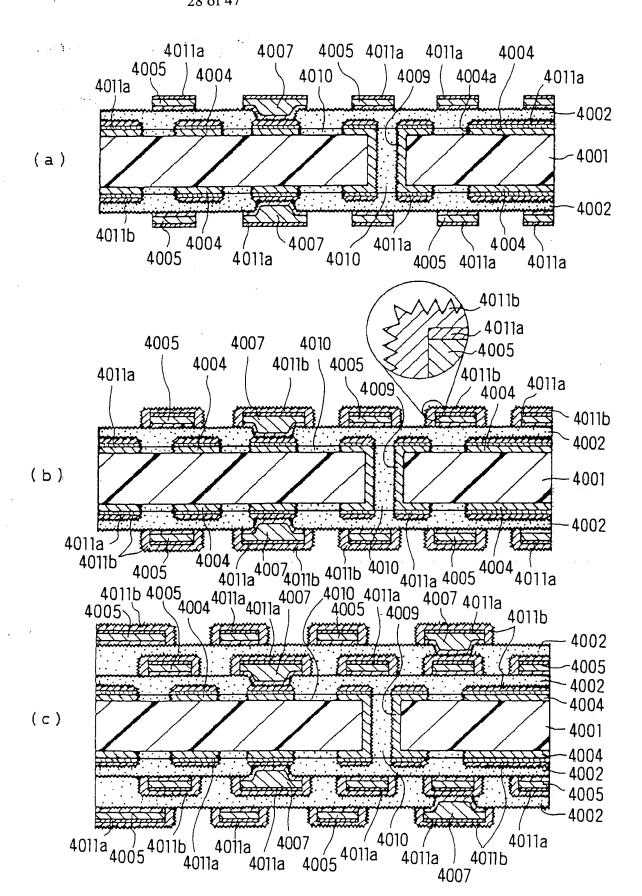
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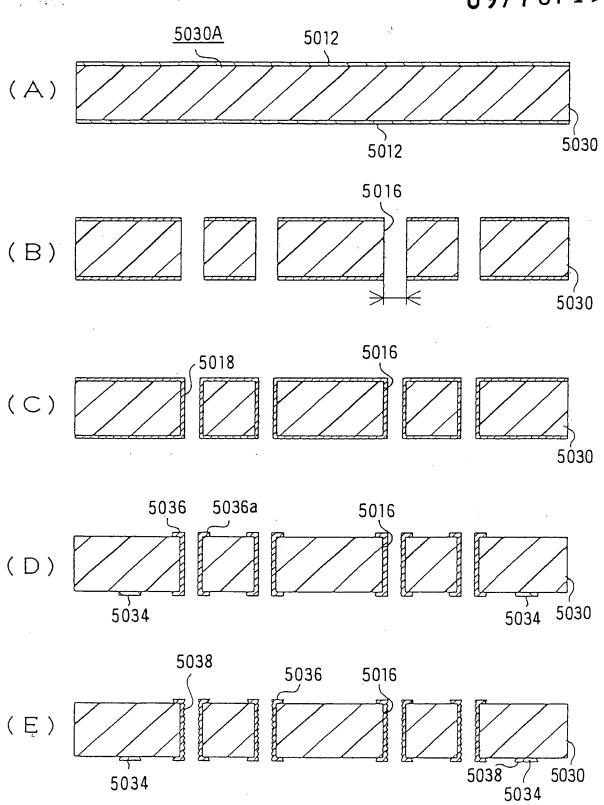
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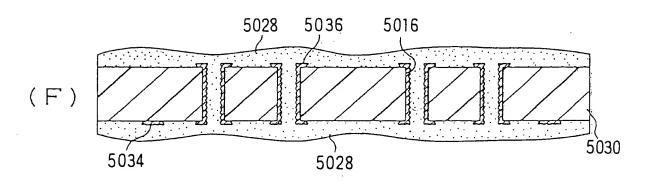


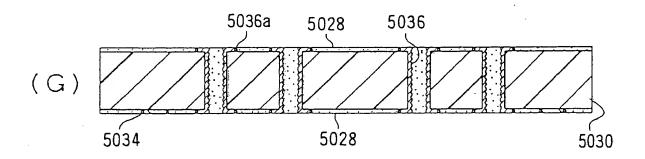
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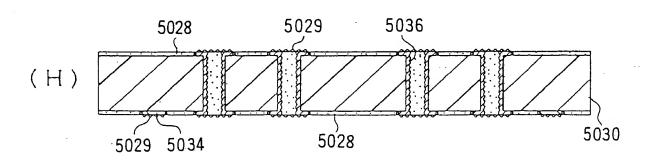
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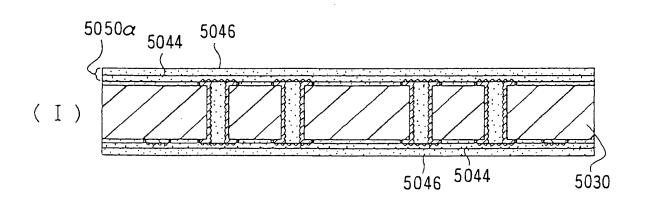
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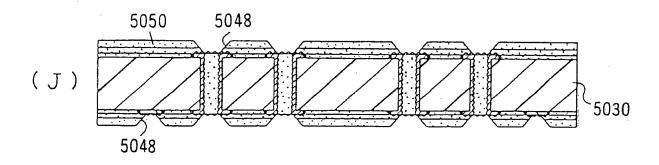


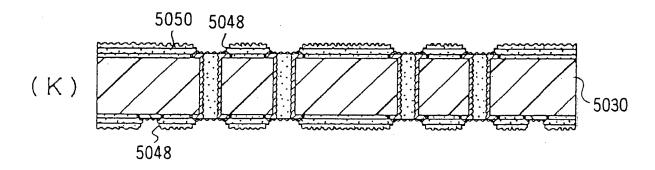
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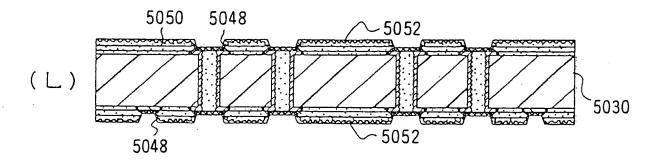
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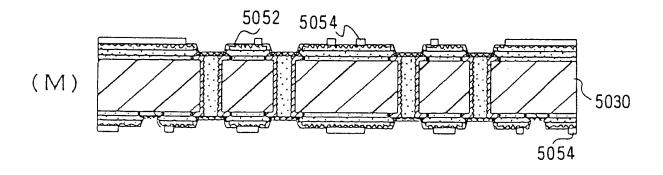
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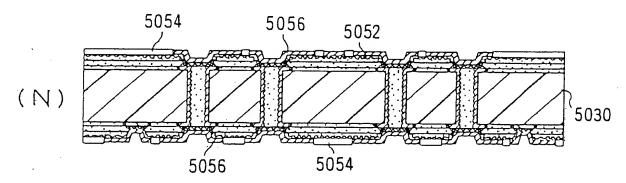


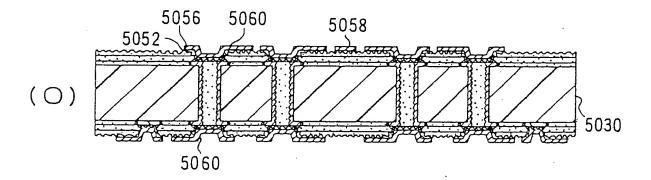


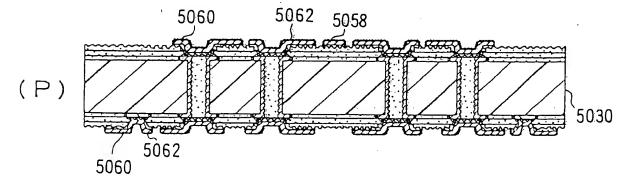


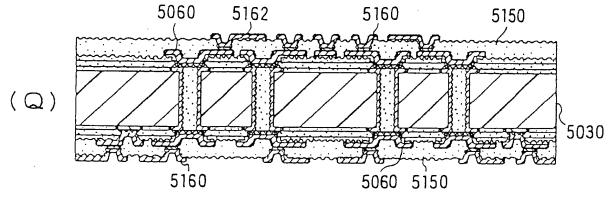
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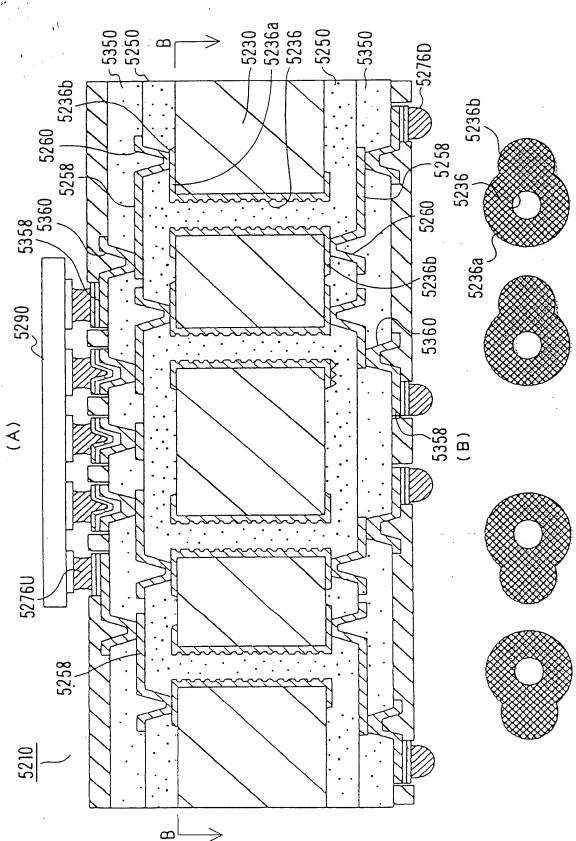
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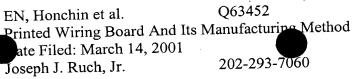
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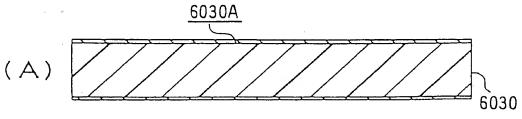
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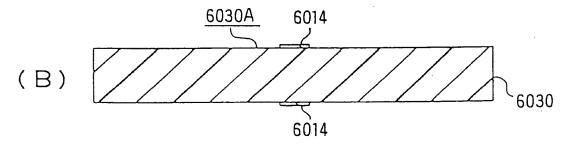
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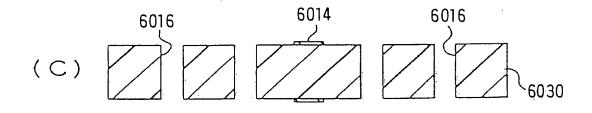
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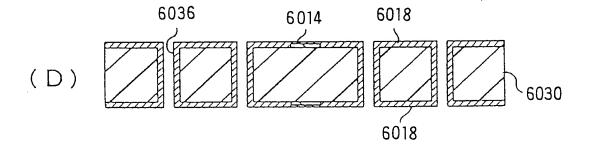


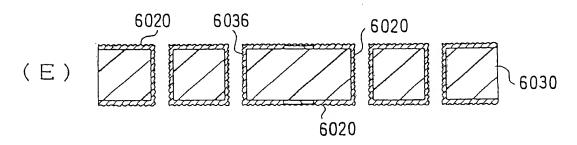








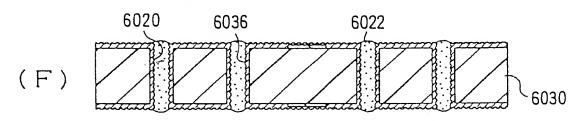




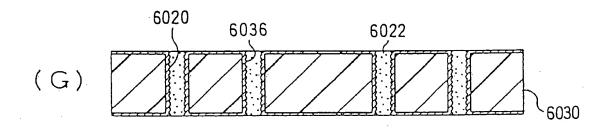


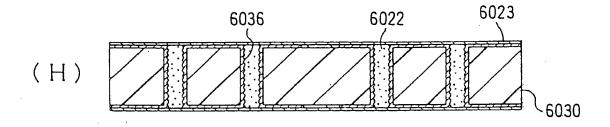
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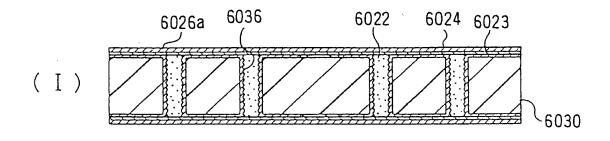
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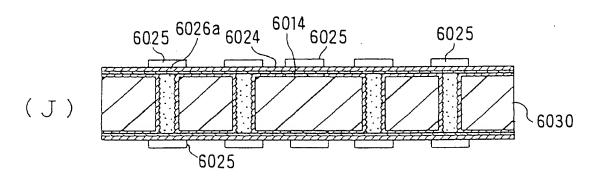
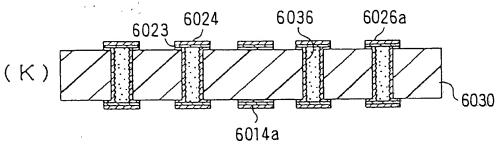


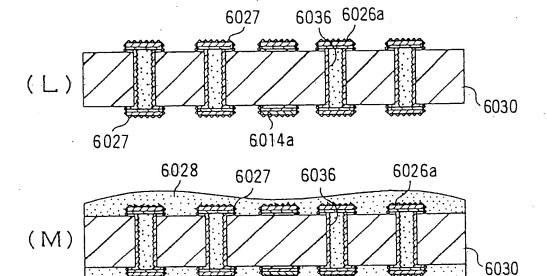
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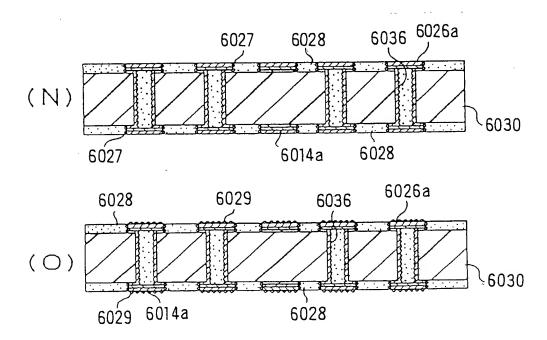


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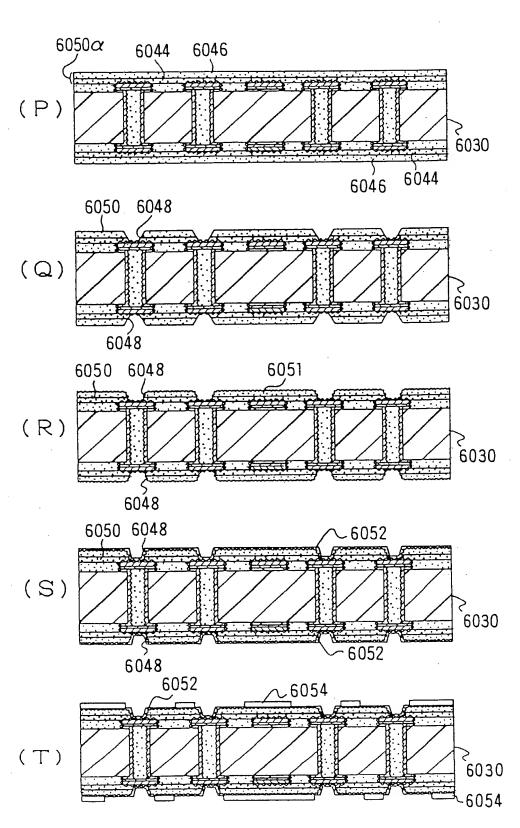
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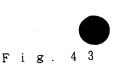
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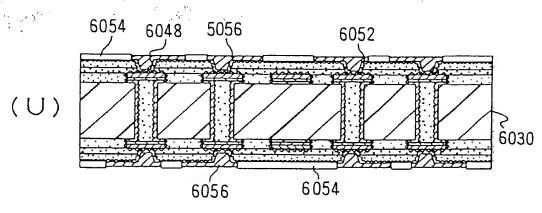
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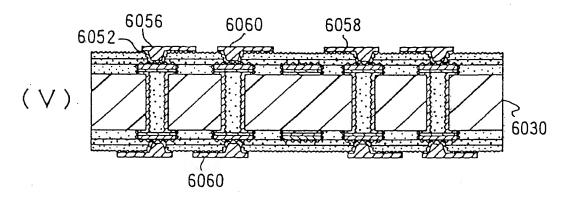
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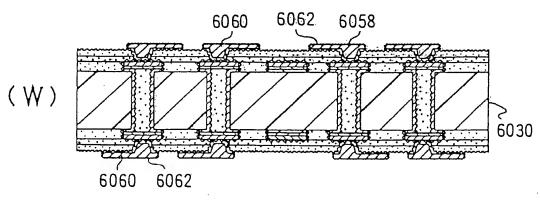
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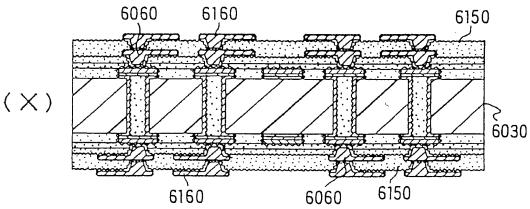
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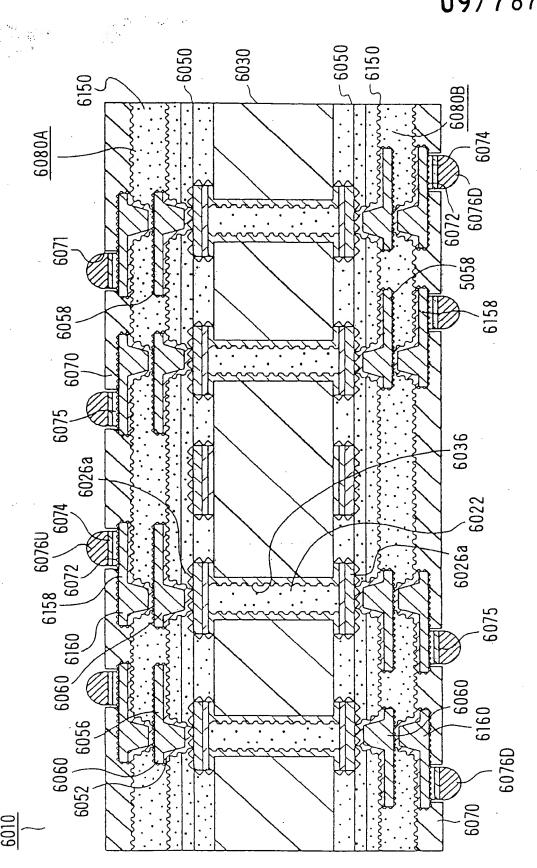




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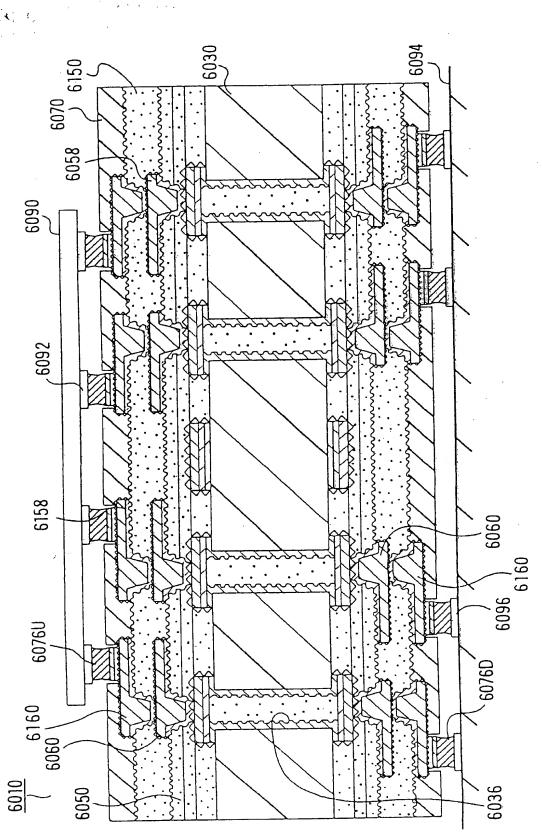




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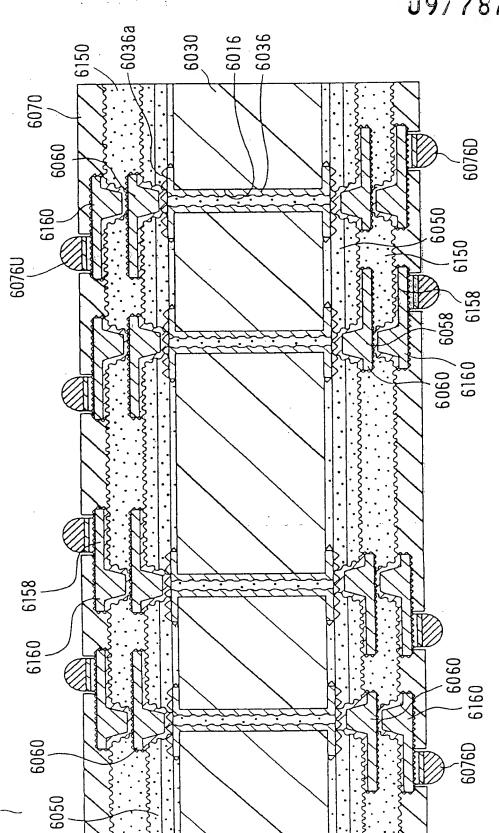


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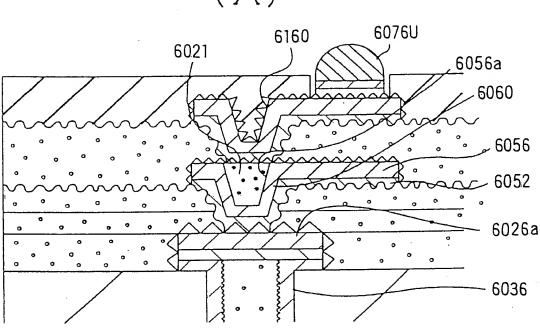
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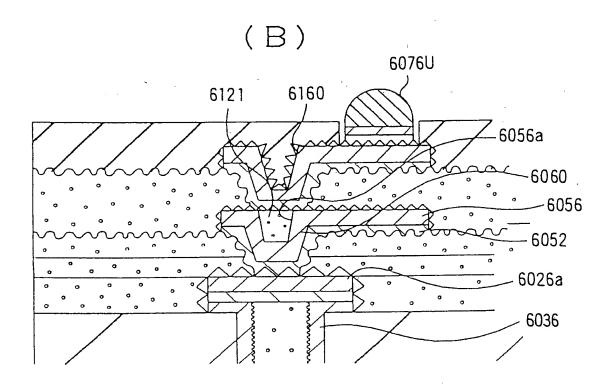
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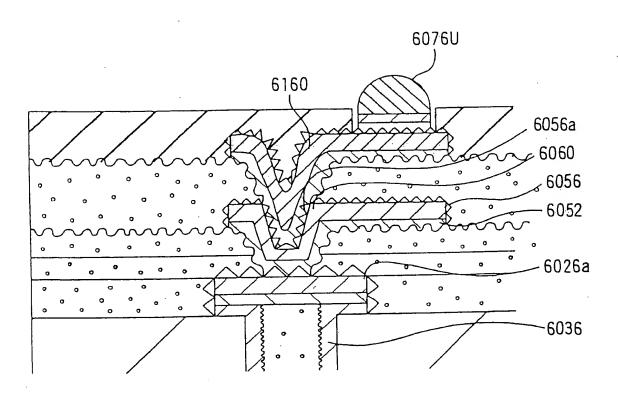
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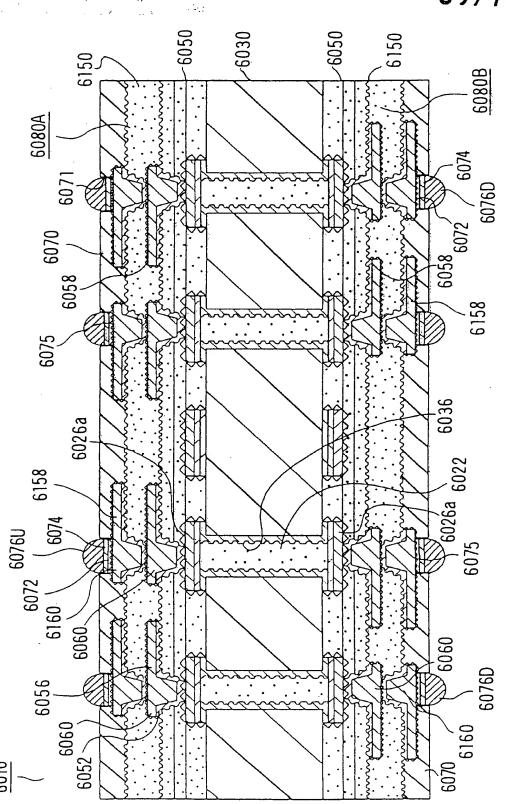
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